

# PVPP College of Engineering, Mumbai

## Department of General Engineering

### Mid-Semester Test SH-2015

Sem-1 All Branches Sub: BEEE Max. Marks: 20

Time: 9-30AM to 11AM Date: Sept. 08, 2015

---

**Note:** (1) Q.1 is Compulsory, (2) Attempt any three questions from Q.2 to Q.5.  
(3) Each question carry 5 Marks. (4) Assume suitable data wherever required with proper justification

---

**Q1** What voltage should be applied to the circuit given below through the adjustable source E?

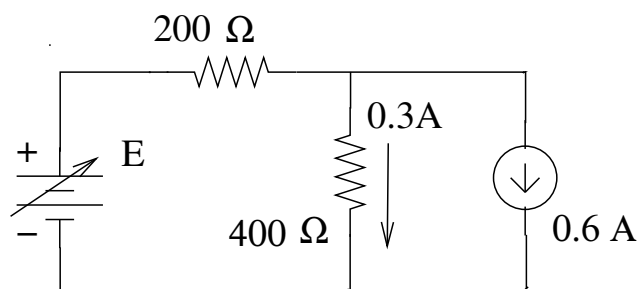


Figure 1: Q.1

**Q2** Find the current through 4Ω resistor by source conversion technique.

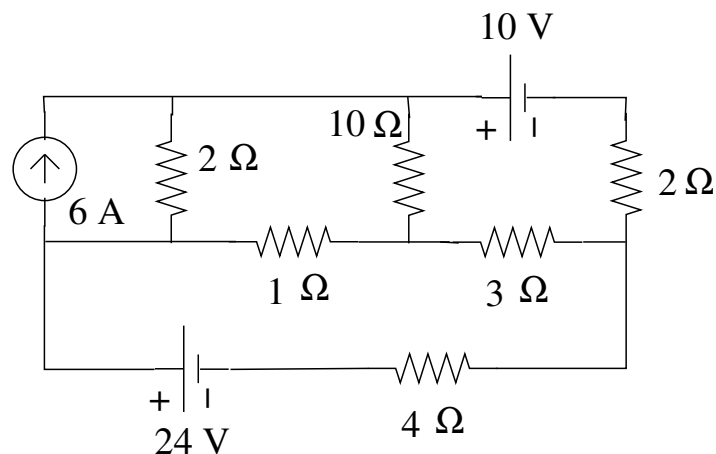


Figure 2: Q.2

**Q3** For the circuit given below, find the power dissipated in  $1\Omega$  resistor by nodal analysis

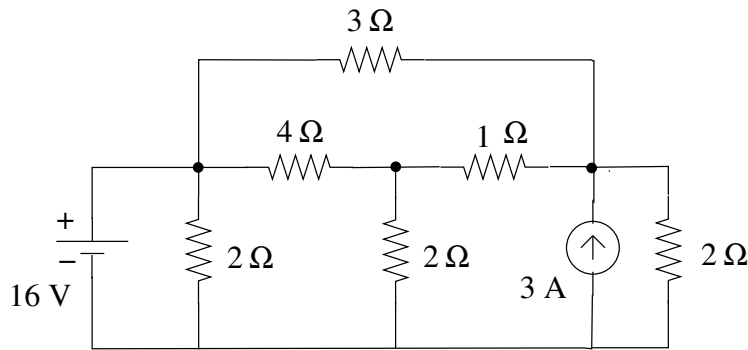


Figure 3: Q.3

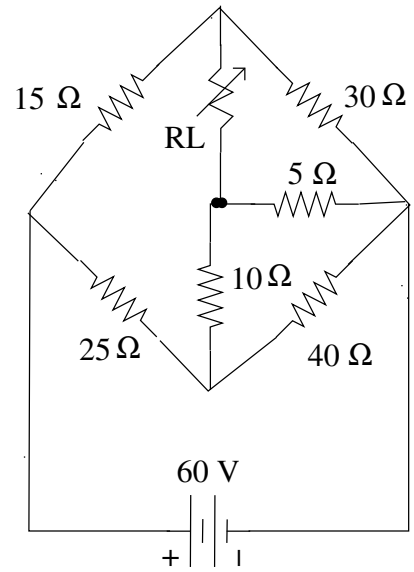


Fig. Q.4

**Q4** Find the value of resistor  $R_L$  so that maximum power will be transferred to it. Also find maximum power transferred to  $R_L$ .

**Q5** Find the current through  $10\Omega$  resistor by using superposition theorem.

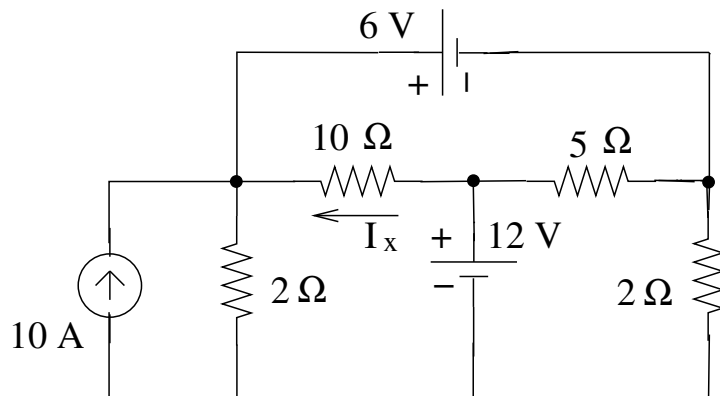
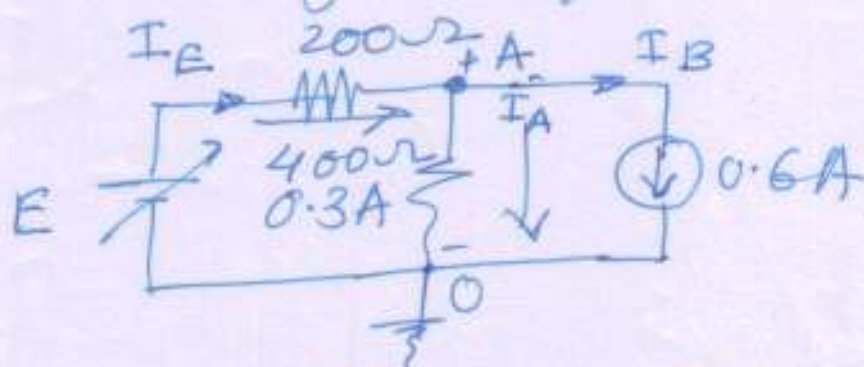


Figure 4: Q.5

----- PAPER ENDS -----

# Answers to the Mid-Sem Test for SH-2015

Q.1 Redrawing the given figure



The voltage  $V_{A0} = 400 \times 0.3 = 120V$

By KCL  $I_E = I_A + I_B$   
 $= 0.3 + 0.6 = 0.9A$

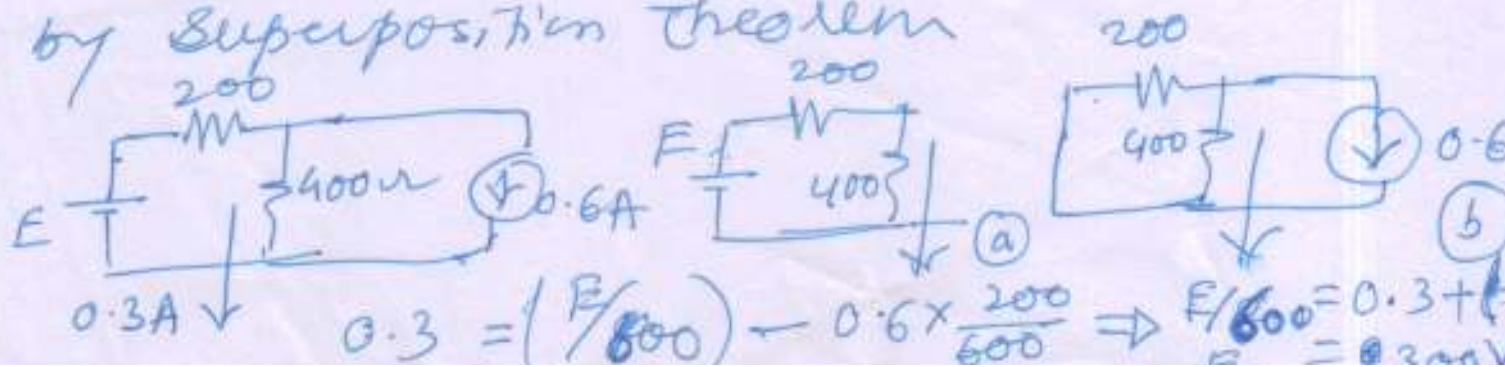
$\therefore$  By Applying KVL around the loop containing E

$$E - I_E \times 200 - I_A \times 400 = 0$$

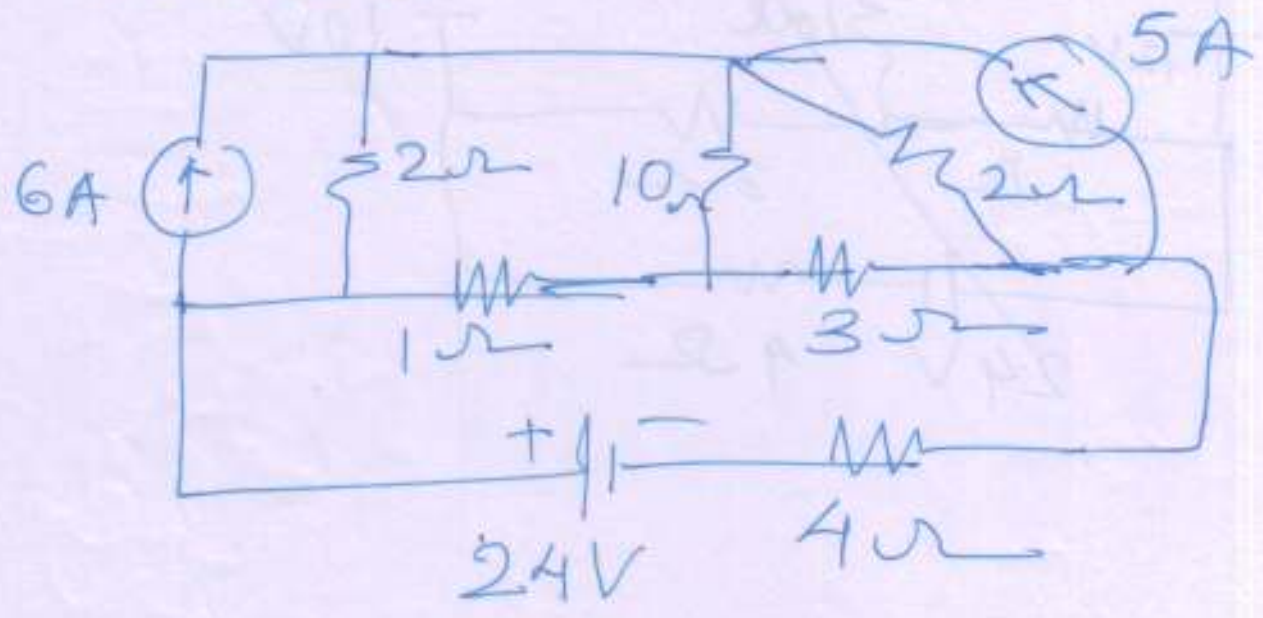
$$\therefore E - 0.9 \times 200 - 0.3 \times 400 = 0$$

$$E = 180 + 120 = 300 \text{ Volt}$$

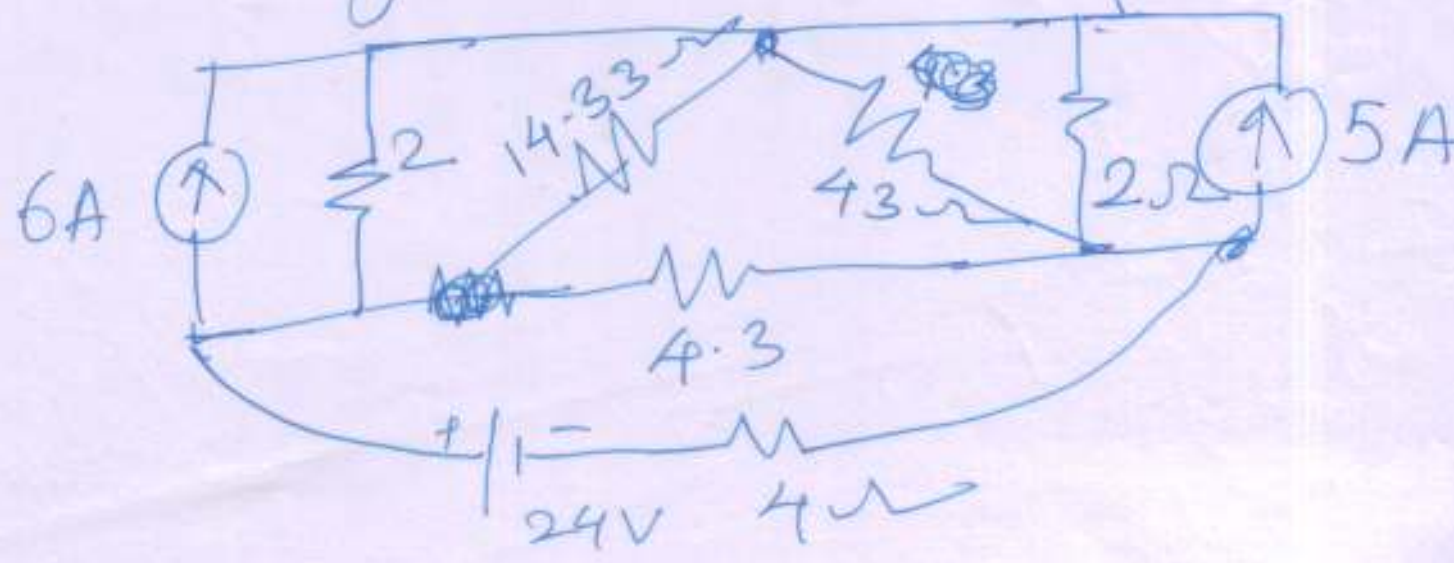
The Problem can ~~also~~ also be solved by Superposition theorem



Q.2 Solving the problem by  
 Source Conversion technique  
 Converting 10V source into current  
 source

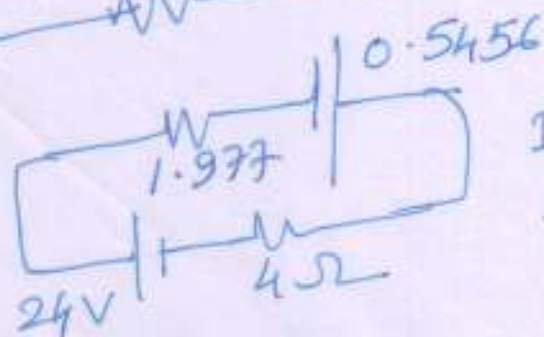
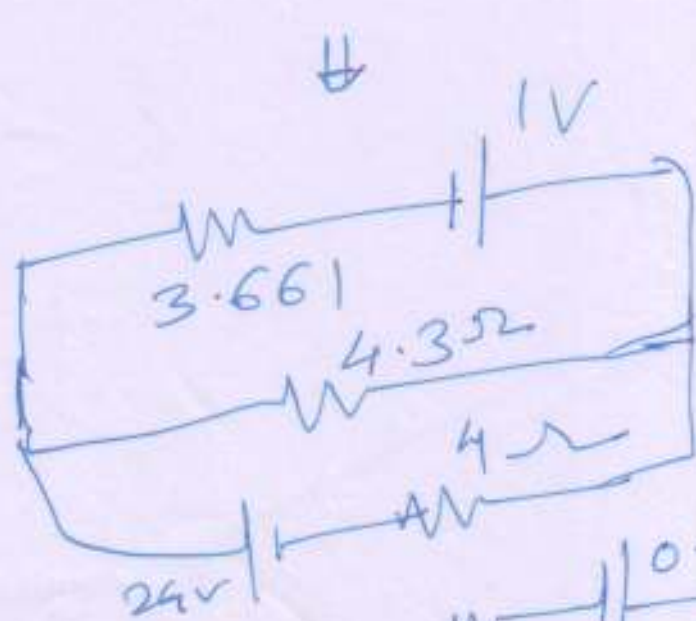
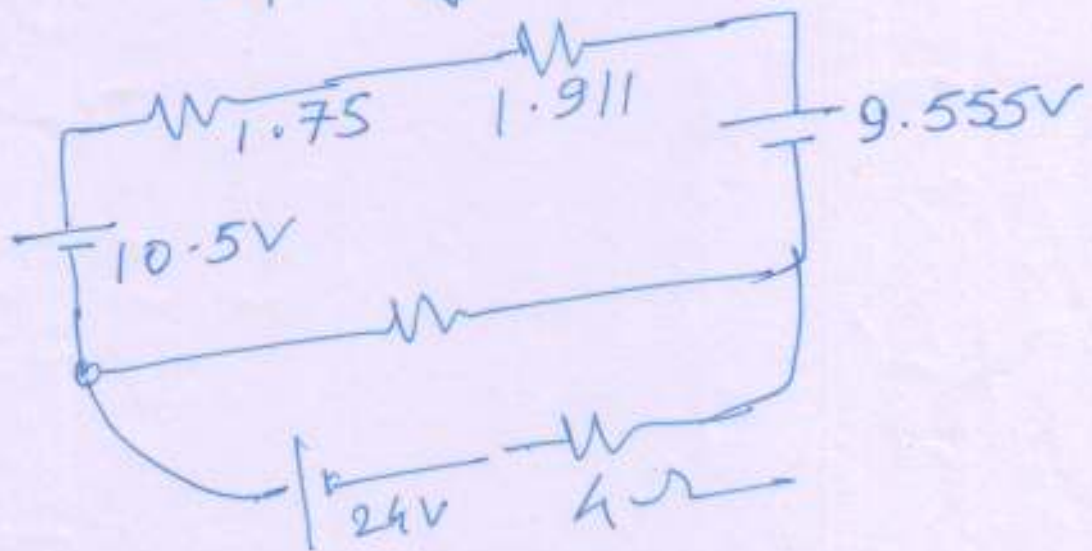
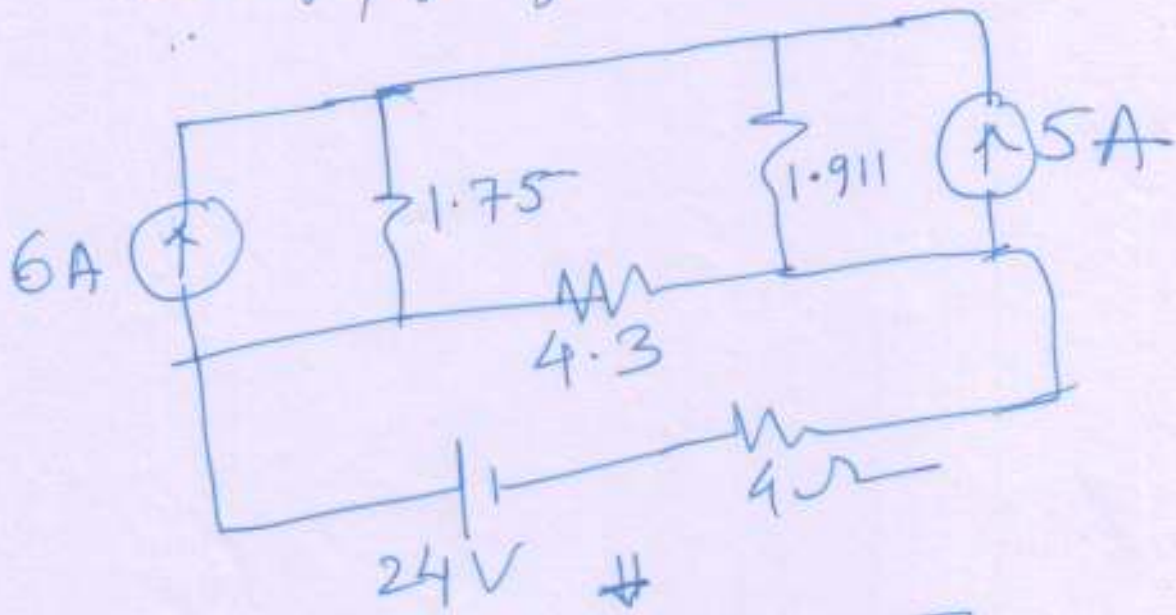


A star network is formed  
 with 10, 1 and 3Ω resistors.  
 Converting  $\Delta$  to  $\Delta$  transformation



Pg-3

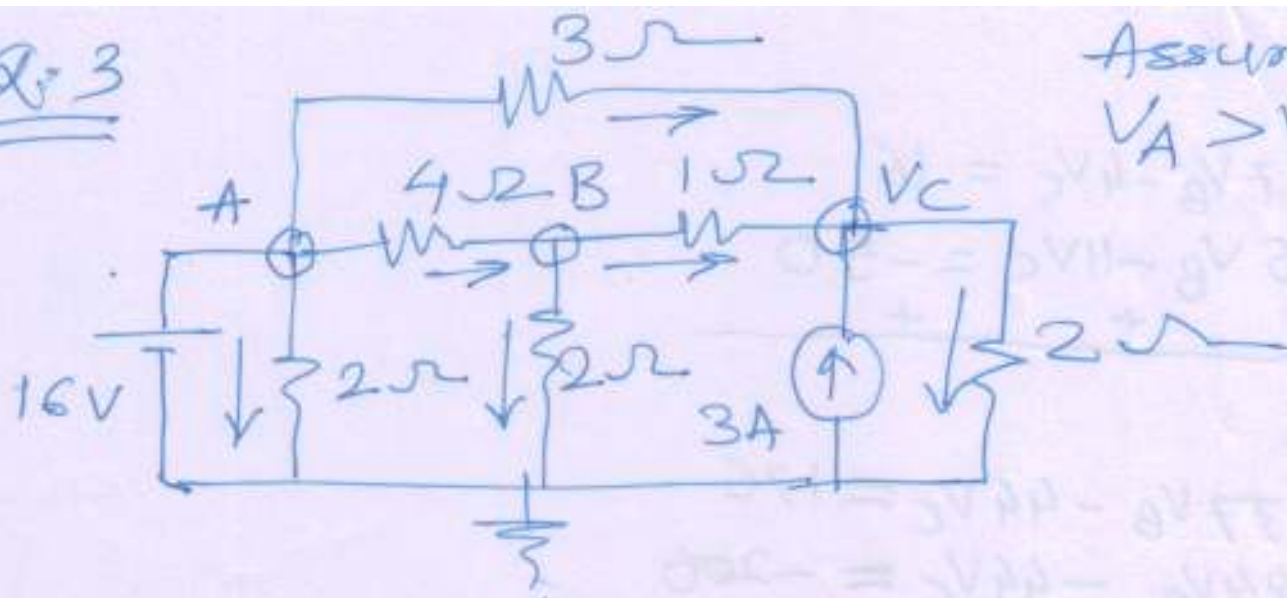
Simplyfing the ckt further



$I_4 = 4.1 \text{ A}$

Q. 3

Assume  $V_A > V_B > V_C$



Node A has 16V battery directly connected to the node A and ground

∴ we can write

$$\boxed{V_A = 16V} \quad \text{--- (1)}$$

\* Writing KCL around node B

$$\frac{V_A - V_B}{4} = \frac{V_B}{2} + \frac{V_B - V_C}{1}$$

$$\therefore V_A - V_B = 2V_B + 4V_B - 4V_C$$

$$\boxed{7V_B - 4V_C = 16} \quad \text{--- (2)}$$

\* Writing KCL eqn. at node C

$$\frac{V_B - V_C}{1} + \frac{V_A - V_C}{3} + 3 = \frac{V_C}{2}$$

$$6V_B - 6V_C + 2V_A - 2V_C + 18 = 3V_C$$

$$6V_B - 11V_C = -18 - 32 = -50$$

-1

$$\boxed{6V_B - 11V_C = -50} \quad \text{--- (3)}$$

$$\begin{array}{r}
 7V_B - 4V_C = 16 \\
 5V_B - 11V_C = -50 \\
 \hline
 \end{array}$$

$$\begin{array}{r}
 77V_B - 44V_C = 176 \\
 24V_B - 44V_C = -200 \\
 \hline
 \end{array}$$

$$53V_B = 376$$

$$\begin{array}{l}
 V_B = 7.09 \text{ Volts.} \\
 V_C = 8.41 \text{ Volts.}
 \end{array}$$

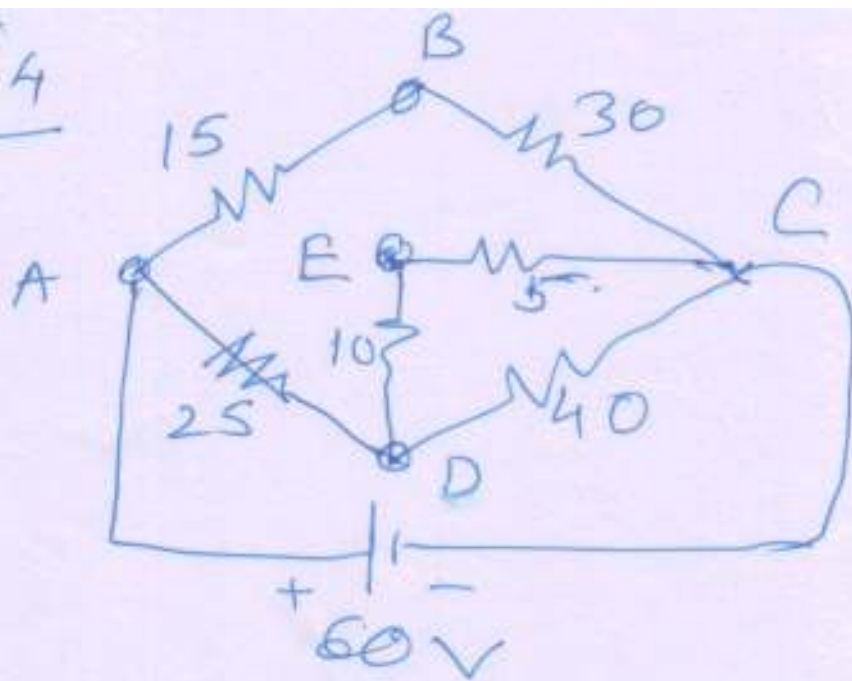
Current through  $1 \Omega$  resistor

$$I_1 = \frac{V_C - V_B}{1} = 1.325 \text{ Amp.}$$

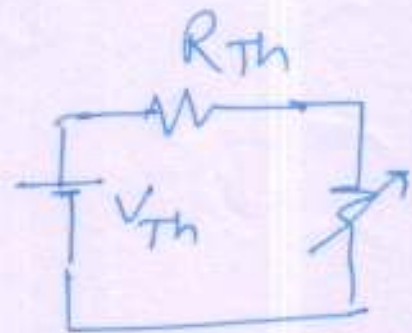
Power dissipated =  $(1.325)^2$

$$P = 1.755 \text{ watt}$$

pg-7  
Q. 4



Opening the  
ckt from R  
Branch

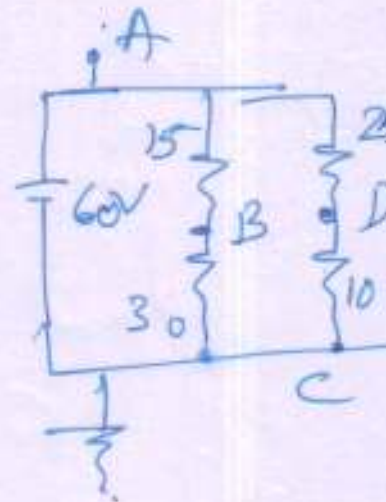
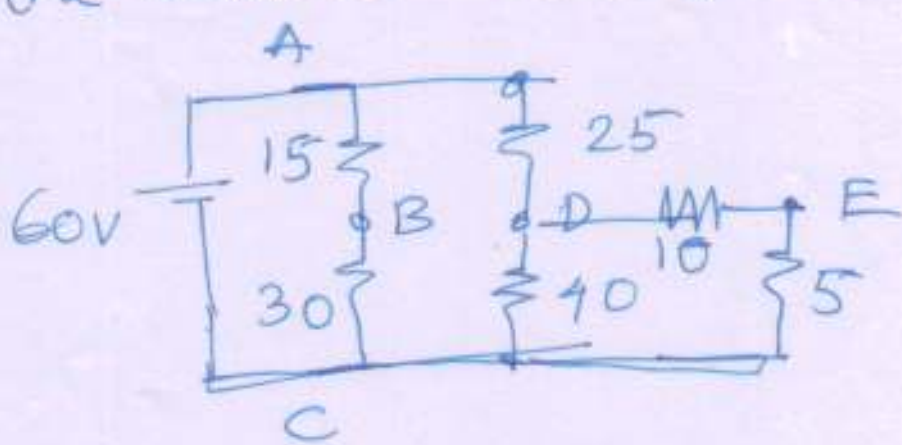


Thevenin's ~~Equ~~  
Circuit

Applying Thevenin's Theorem.

a) To find  $R_{Th} = V_{BE}$

the equivalent ckt is drawn as.



$$V_{Th} = V_{BE} = V_{BC} - V_{EC}$$

$$V_{BC} = 60 \times \frac{30}{45} = 40 \text{ Volts}$$

[∵ By Potential Divider  
Rule]

$$V_{EC} = V_{DC} \times \frac{5}{15}$$

$$V_{DC} = 60 \times \frac{10.9}{35.9}$$

$$= 6.07 \text{ Volts}$$

$$= 18.21 \text{ V}$$

$$\therefore V_{Th} = 40 - 6.07 = 33.93 \text{ Volts}$$

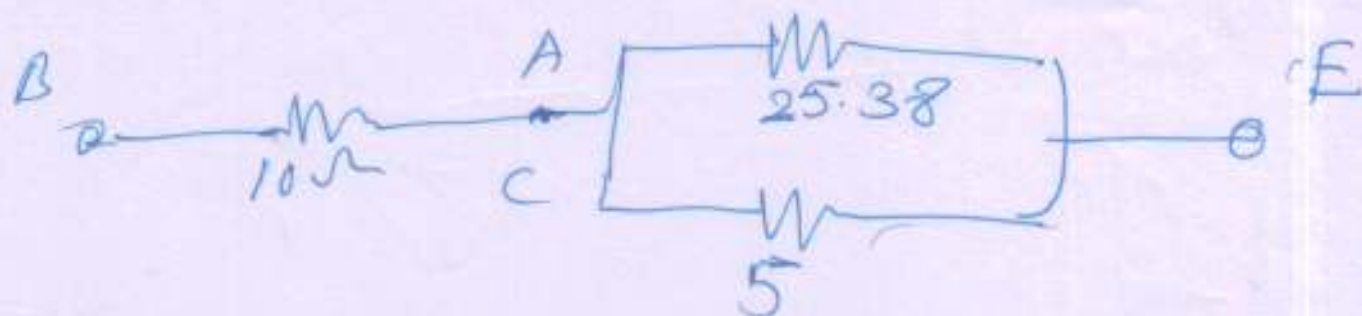
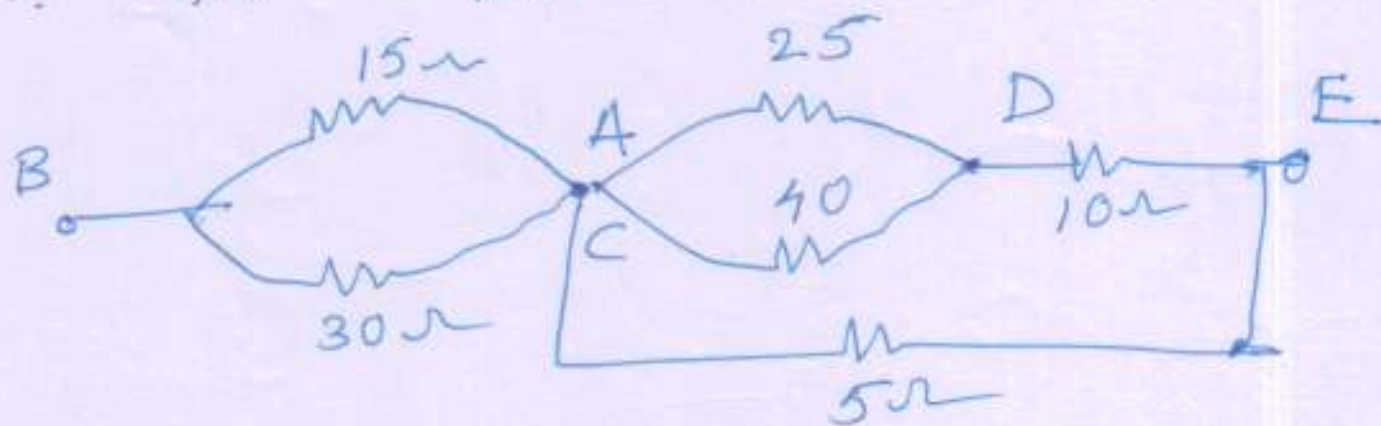


Q.4 ... Continued ...

To find  $R_{Th}$ .

- A and C terminals are shorted
- These nodes are joined together to find an equivalent resistance looking from B and E. The developed diagram is as follows:

$\therefore R_{Th} = R_{BE}$



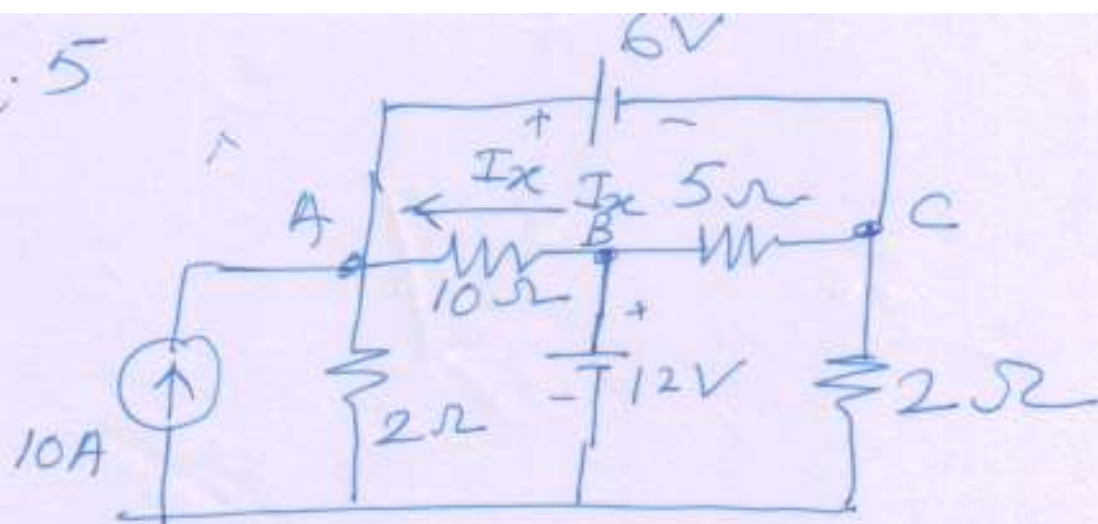
$R_{BE} = V_{Th} = 14.17 \Omega$

$\therefore$  The resistor  $R_L$  has value equals to  $R_{BE}$  or  $V_{Th}$  to draw Maximum power from the source in  $R_L$ .  $R_L = 14.17 \Omega$

$P_{max} = \frac{V_{Th}^2}{4 R_L} = \frac{(33.93)^2}{4 \times 14.17} = \underline{\underline{20.31 \text{ Watt}}}$

Q. 5

Pg 9



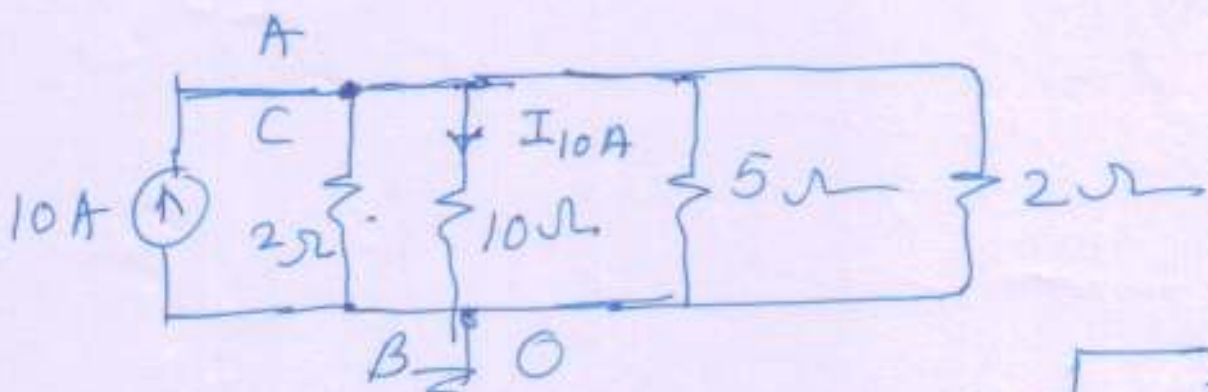
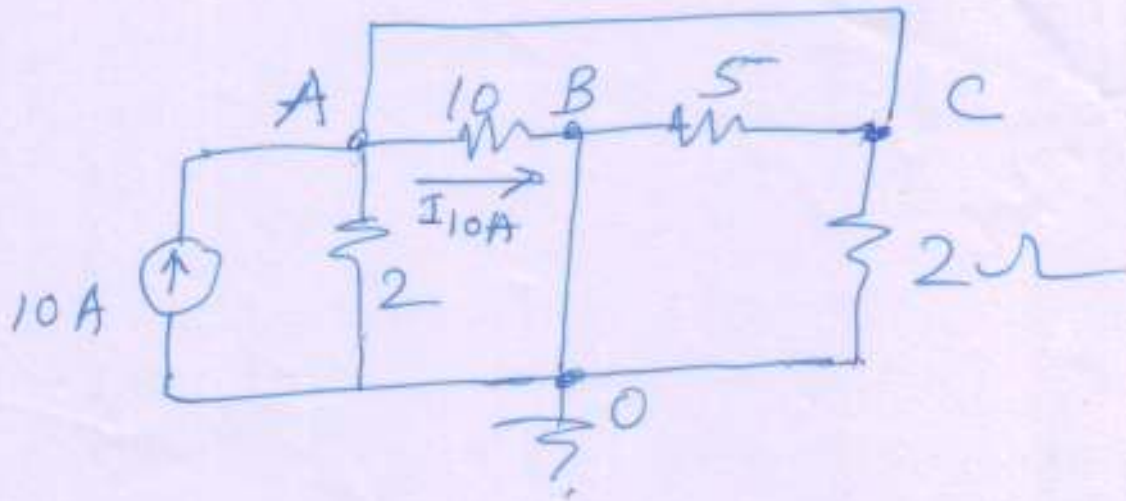
To find  $I_x$  ~~using~~ by using Superposition Theorem, we will have to solve three circuits ~~and~~ to find three currents  $I_{10A}$ ,  $I_{12V}$  and  $I_{6V}$  by considering individual sources only, replacing meanwhile all other sources by their internal resistances. Finally

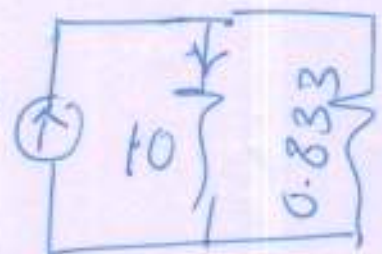
$$I_x = I_{10A} + I_{12V} + I_{6V}$$

with appropriate sign-convention for the currents.

If the individual current is in the same direction with  $I_x$  assumed the current is considered as positive, otherwise, the current is taken as negative.

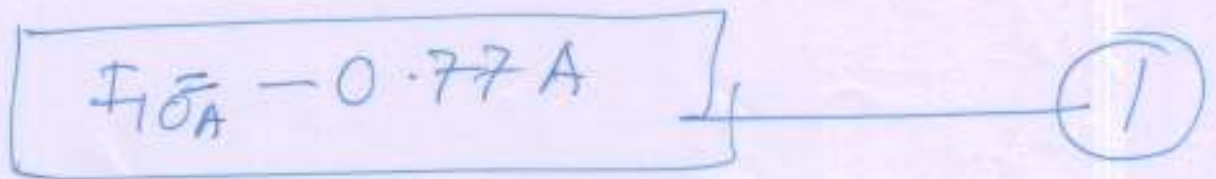
a) Circuit with 10A Source only



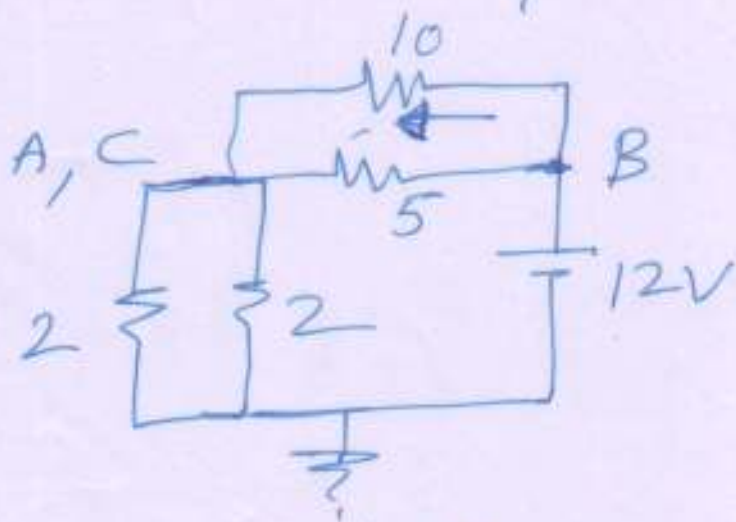
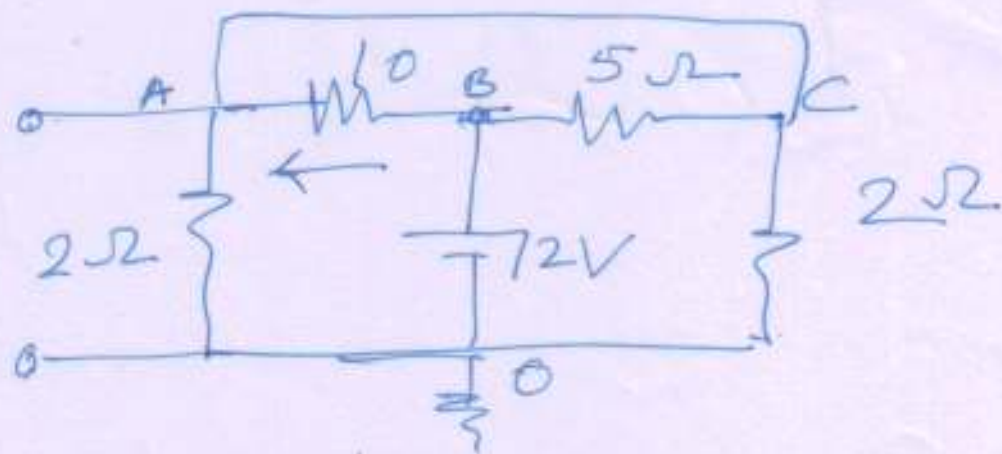
By Current division Rule. 

$$I_{10A} = 10 \times \frac{0.833}{10 + 0.833} = 0.769 \text{ A} \approx 0.77 \text{ A}$$

$I_{10A}$  has Opposite Polarity  $\therefore I_{10A} = -0.77 \text{ A}$



b) Circuit with the 12V source only



$$V_{BA} = 12 \times \frac{10 \parallel 2}{10 \parallel 2 + 5}$$

$$\therefore V_{BA} = 12 \times \frac{3.33}{4.33} = 9.23V$$

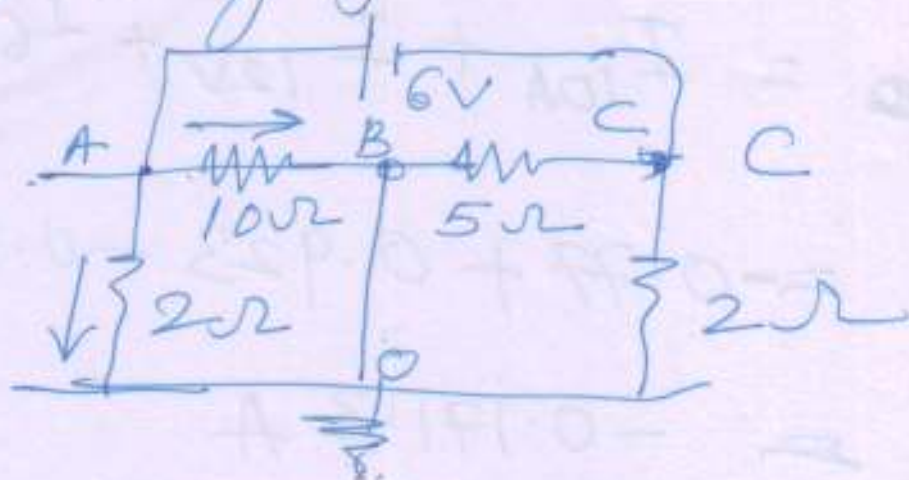
$$\therefore I_{12V} = \frac{9.23}{10} = \underline{0.923 \text{ A}}$$

Since  $I_{12V}$  is having ~~opposite~~ the same sign to that of  $I_{oc}$  (assumed)

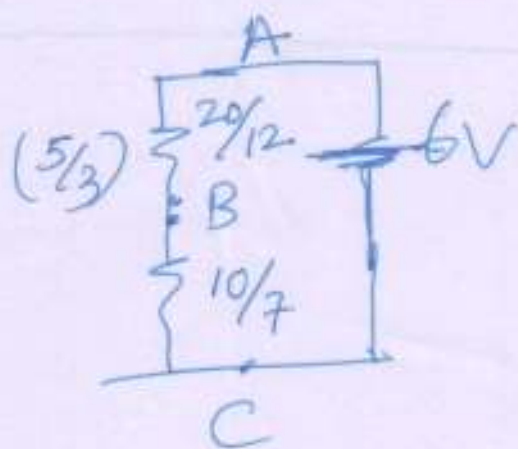
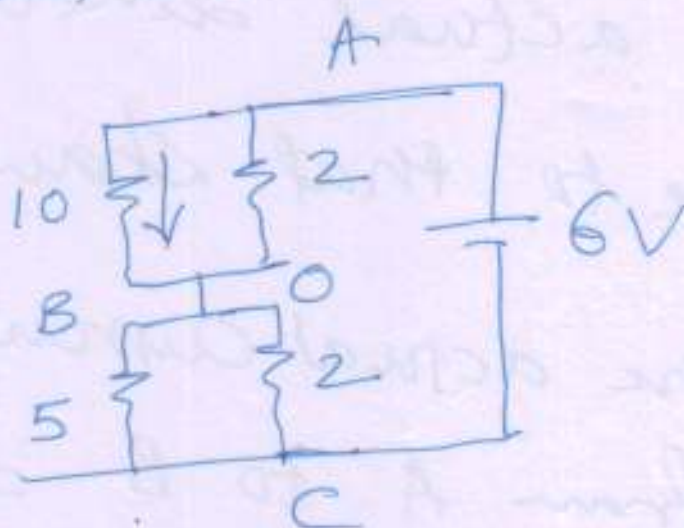
$\therefore I_{12V}$  is positive

$$I_{12V} = 0.923 \text{ A}$$

e) Considering the source 6V only



Equivalent ckt



$$\begin{aligned} \therefore V_{AB} &= 6 \times \frac{5/3}{5/3 + \frac{10}{7}} \\ &= 6 \times \frac{1.67}{1.67 + 1.4285} \\ &= 3.243 \text{ V} \end{aligned}$$

$$\therefore I_{6V} = \frac{3.243}{10} = 0.3243$$

since the direction (sign)  $I_{6V}$  is negative

$$\therefore I_{10} = -0.3243$$

Thus By Superposition theorem

$$I_{AB} = I_{10A} + I_{12V} + I_{6V}$$

$$= -0.77 + 0.923 - 0.3243$$

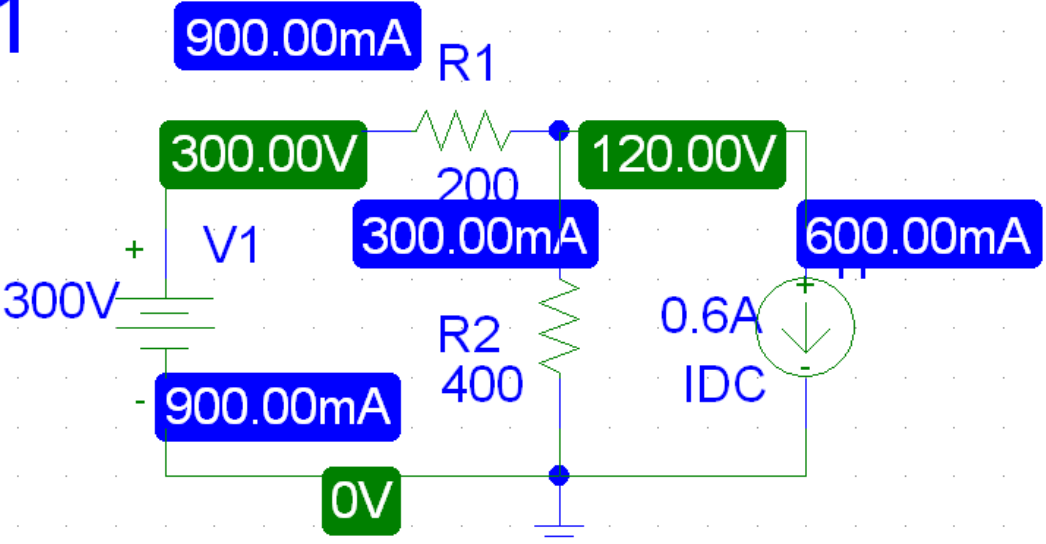
$$I_{AB} = -0.1713 \text{ A}$$

∴ the actual direction is opposite to that shown.

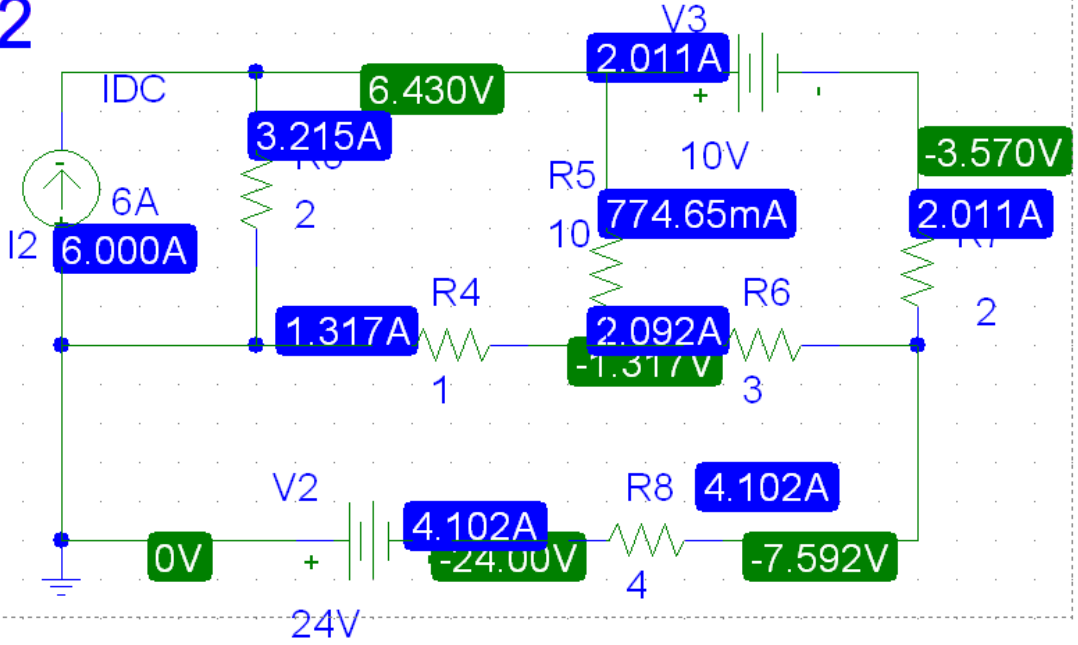
∴ the actual current direction  
 is from A to B = 0.1713 A

# BEEE Simulation Results Midsem Test Paper SH2015

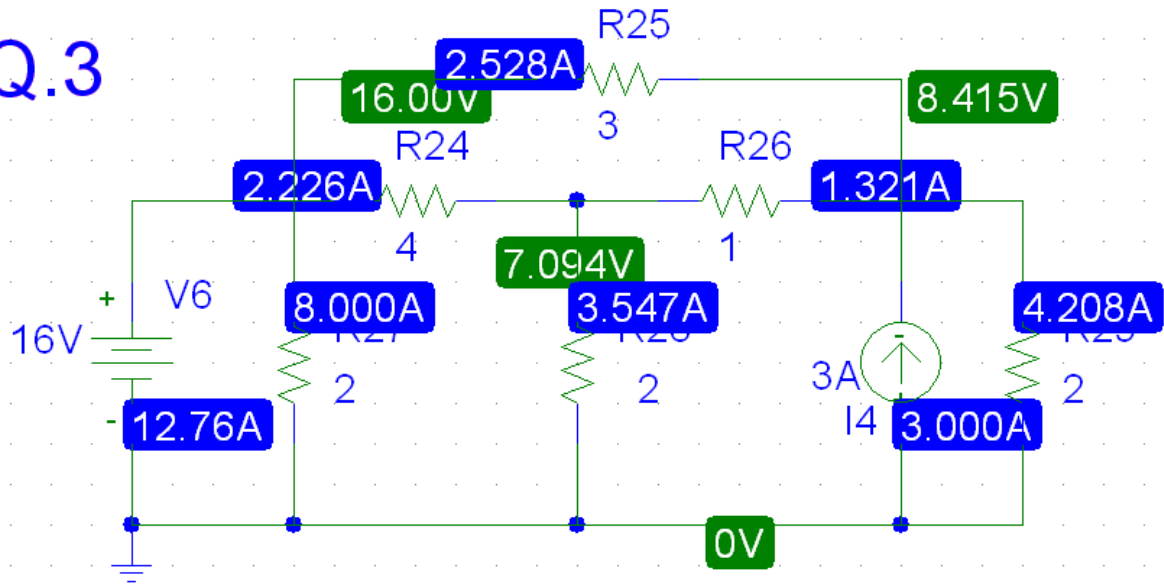
Q.1



Q.2

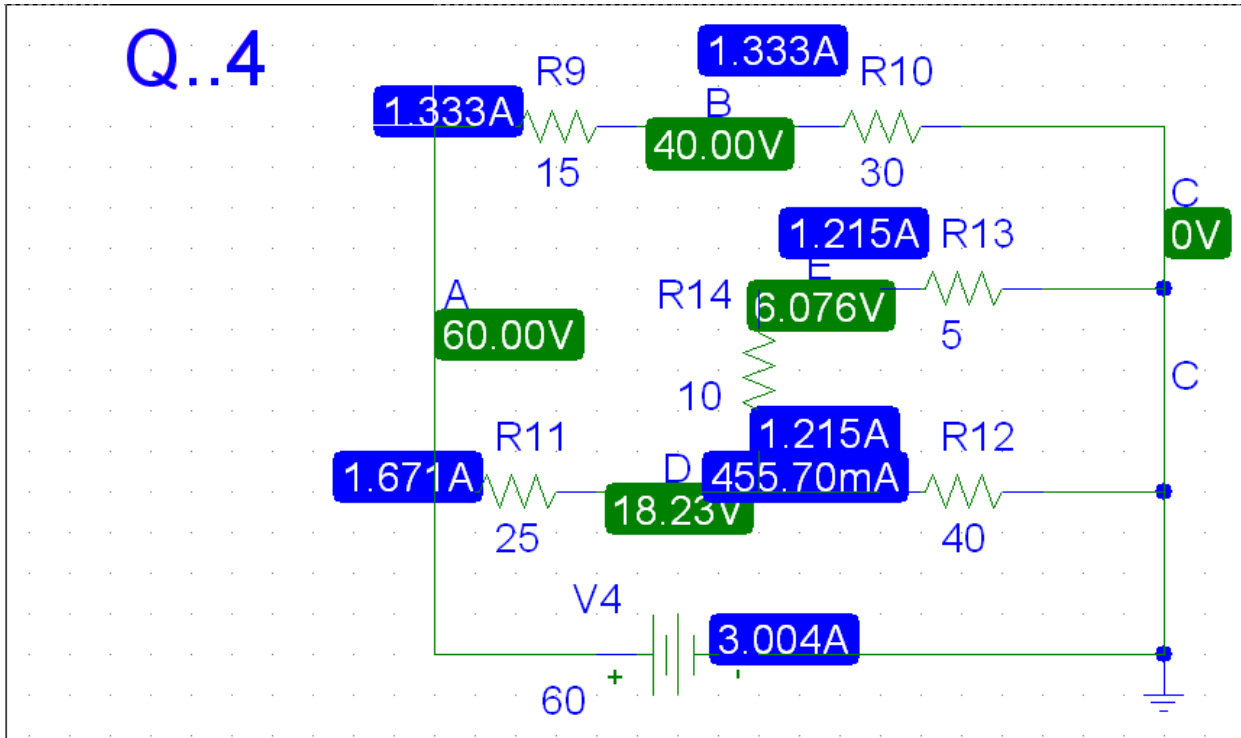


Q.3

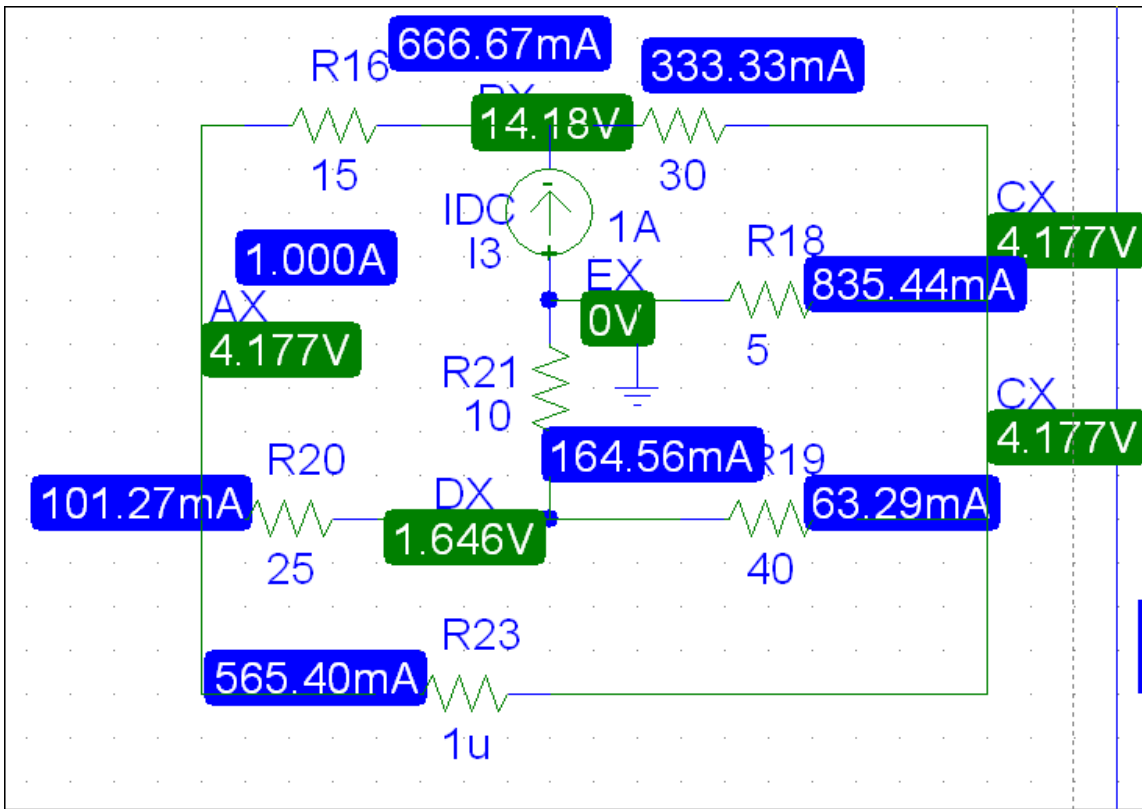




Q..4

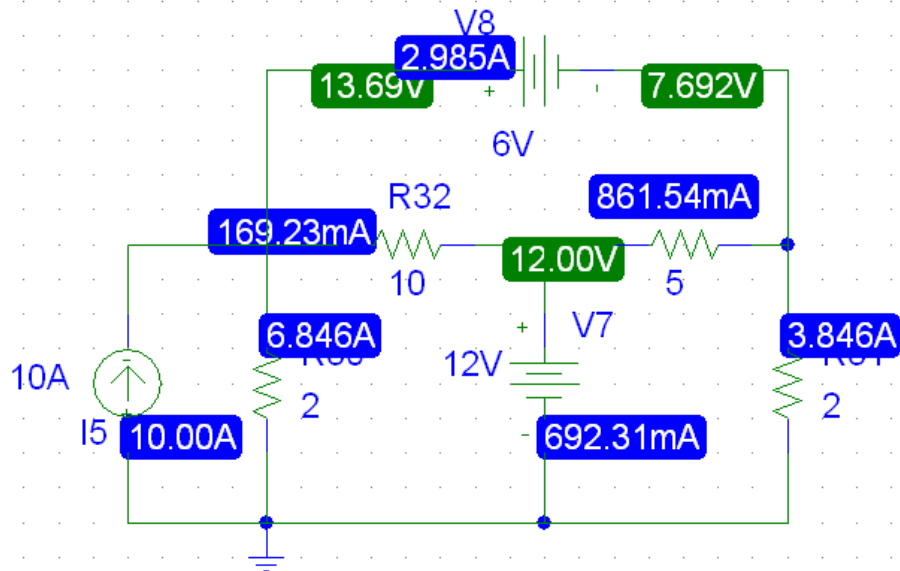


Upper Circuit for VTH Calculation

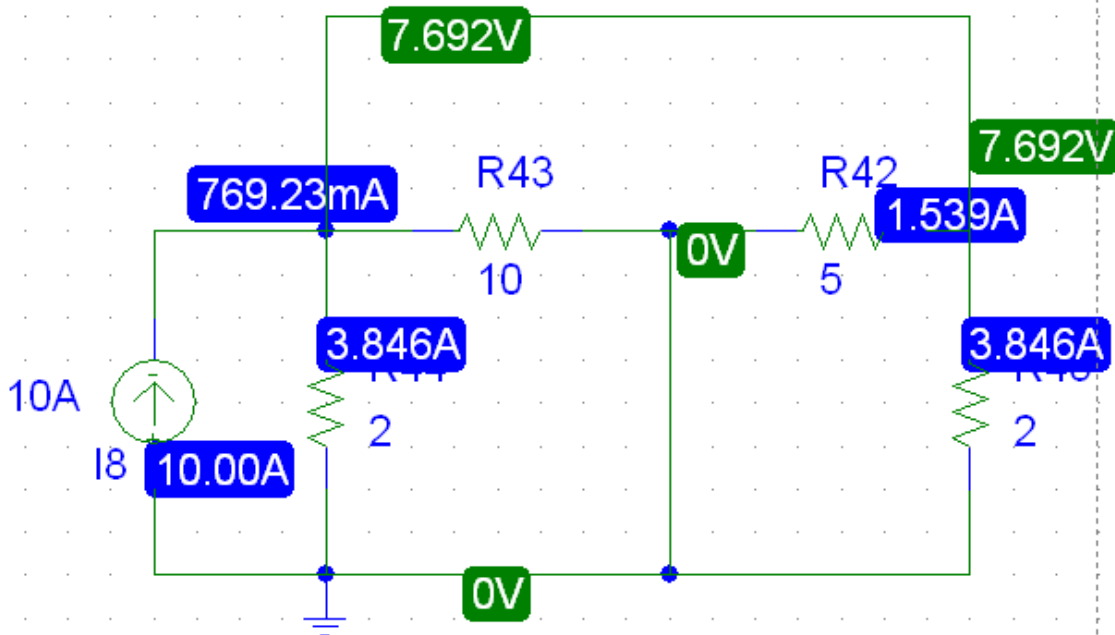


The Circuit for RTH Calculation

Q..5



MAIN CIRCUIT



CIRCUIT with 10A

